Enhanced Simulated Evolution Algorithm For Digital Circuit Design Yielding Faster Execution In A Larger Solution Space

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Summary

Evolutionary algorithms have been studied by several researchers for the design of digital circuits. Simulated evolution (SimE) is used in This work due to it simplicity and customizability to combinatorial problems. A tree data structure based circuits are evolved. Thus, a larger solution space is investigated. In addition, a new pattern based goodness measure is presented.